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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/612,414	07/02/2003	Yoshio Kurosawa	1324.68134	3620

7590 01/10/2007
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EXAMINER

ERDEM, FAZLI

ART UNIT	PAPER NUMBER
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2826

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	01/10/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

10/612,414

Applicant(s)

KUROSAWA ET AL.

Examiner

Fazli Erdem

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 December 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 5-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 7 and 8 is/are allowed.
- 6) ☒ Claim(s) 5 and 6 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--------------------------------------------------------------------------------------|-------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

A certified translation of the priority document for the current application was filed. Therefore, Satou prior art that was used in making the final rejection cannot be used anymore. Hence, the final rejection issued on 10/23/2006 has been withdrawn. However, after further search and consideration this action is issued and made non-final.

Allowable Subject Matter

1. Claims 7 and 8 allowed.

DETAILED ACTION

Allowable Subject Matter

2. Claims 7 and 8 allowed.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 5 and 6 rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki et al. (6,603,453) in view of further in view of Yamazaki et al. (2001/0052950) further in view of further in view of Matsumoto (5,396,084)

Regarding Claims 5 and 6, Yamazaki et al. ('453) disclose a semiconductor device and method for manufacturing the same where in Fig. 1, a P channel TFT and an N channel TFT are disclosed on a substrate 801. Furthermore, P channel TFT has a first gate insulation film 805, a second gate insulating film 808, gate electrode 813 on the second

gate insulation film. N channel TFT also has a first gate insulation film and a gate electrode 814 disposed on the second gate insulation film 809. Yamazaki et al. fail to disclose the required gate insulating layer over semiconductor layer, the required lightly doped regions and the required gate electrode between gate insulating layers and the required configuration of the first and second gate insulating films. However, Yamazaki et al. ('950) disclose s semiconductor display device and manufacturing method thereof where in claims 1, 2, 3, 16 and 19 the required gate insulating layer over semiconductor layer and the required lightly doped regions are disclosed. Matsumoto disclose a thin film transistor device having driving circuit and matrix circuit where in Fig. 1, second gate electrode 16 is located between the first gate insulating layer 14 and the second gate insulating layer 19.

It would have been obvious to one of having ordinary skill in the art at the time the invention was made to include the required insulating layer over semiconductor layer with lightly doped regions and the required gate electrode between gate insulating films and the required configuration of the first and the second gate insulating films in Yamazaki et al. as taught by Yamazaki et al. ('950) et al and Matsumoto, respectively, in order to have a liquid crystal panel display device with flexibility in terms of insulating film thickness and locations.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fazli Erdem whose telephone number is (571) 272-1914. The examiner can normally be reached on M - F 8:00 - 5:00.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

FE

January 7, 2007


EVAN PERT
PRIMARY EXAMINER